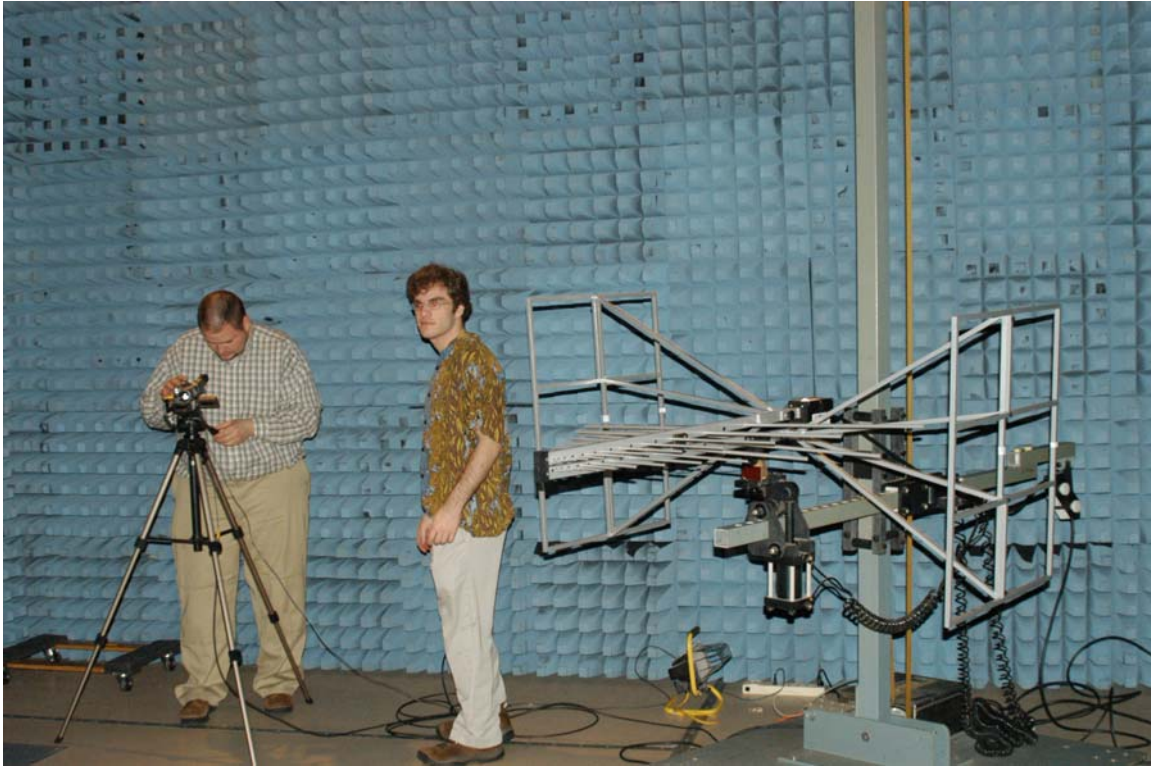




I n n e r S p a c e S y s t e m s  
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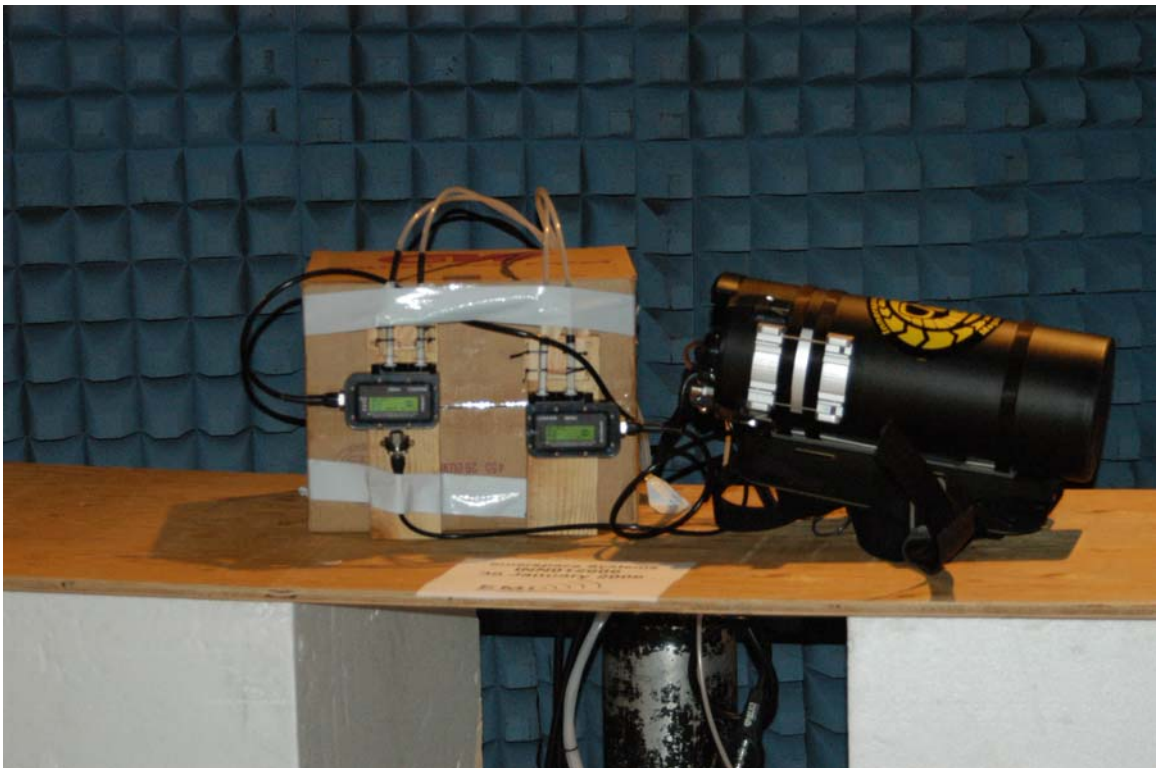
**Friday, 21 March, 2008**



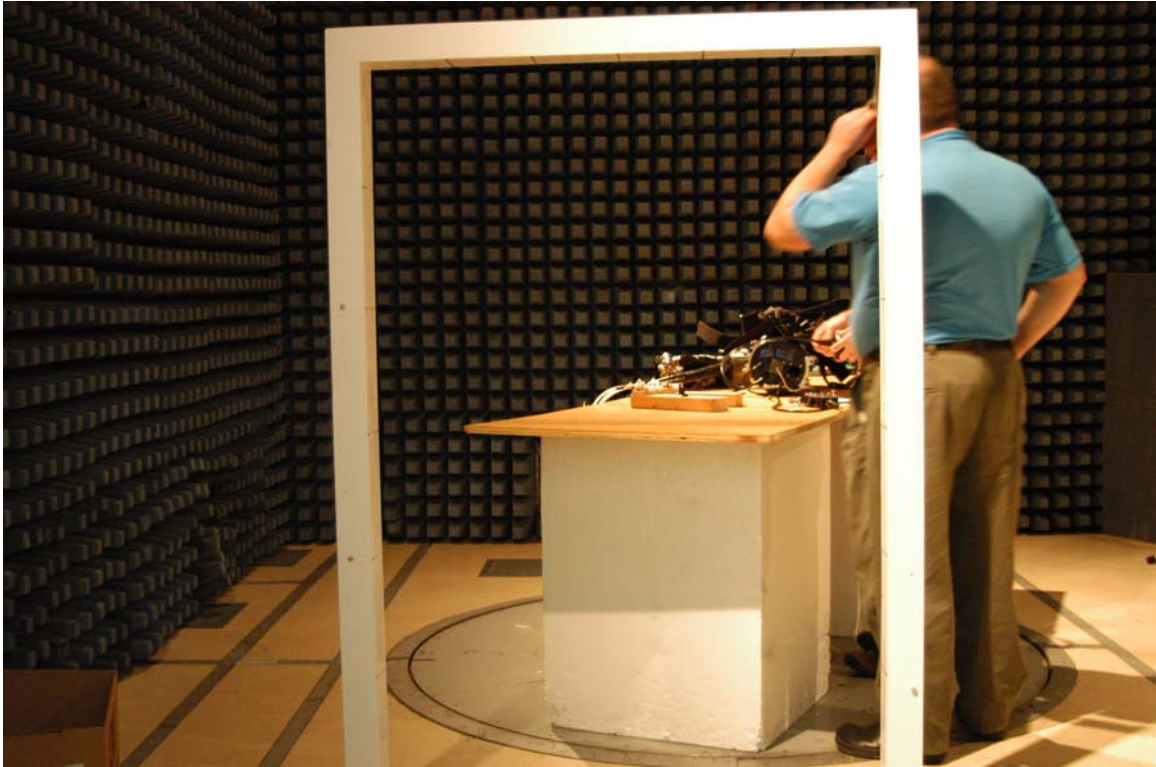
**Photo 1 - Inside EMIs Test Chamber**



**Photo 2 - MEG setup in chamber**



**Photo 3 - APECS 2 electronics test setup**



**Photo 4 - Magnetic Test Setup**



**Photo 5 - Preparing APECS2 for ESD Static Discharge Testing**



**Photo 6 - The ESD tester - top static test discharge is 15,000 volts**



**Photo 7 - System must recover normally after each static discharge**